

Exhibit A

of

37 C.F.R. § 1.131 Declaration

of Alan L. Lasneski

8-15-95

MERLIN

3 LEVELS -

PRIVATE FUNCTIONS

Locking unlock, Register Set,

Shadow, Writable Regs.

- Access to Registers - in PSDB - TABLE INITIALIZATION.

ENCAPSULATION - 10 BIT REGISTERS

OBJECT DECOMPOSITION -

SERIAL COMMUNICATION

ACCESS REGISTERS VIA SERIAL COMMUNICATION
FOR DEBILITATING PURPOSES.

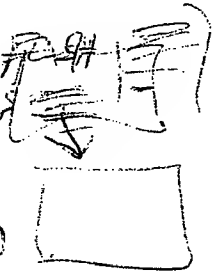
PSDB

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MODE TABLE

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MERLIN



Home Work

FLASH A A TIMES.

8-18-95

Bootblock → Flash download

REWORKED Btcode took out MULTITASK KERNEL

new code size 4.5 K NO Post test type.

timed download

$$2 \text{ megabit} = 262144$$

$$\text{file size} = 96 \text{ K} = 98304$$

$$\frac{98304}{262144} = .375$$

$$\frac{1}{.375} = 2.6666667$$

for 96 K file

@ 9600 baud	= 6 min	* 2.6667
@ 19.2 K baud	= 3.75 min	* 2.6667
@ 38.4 K baud	= 2.5 min	* 2.6667

=	16 min for full 2 Mbit
=	Down
=	6.6 min

Merlin Drivers + Assembly Routines

8-31-95

- I2c - Implemented - WRITE ONLY.
NEED READ ROUTINES -

PLL - WRITE + READ Implemented
NOTE: CANNOT READ BACK TO PROCESSOR THRU IS
+ LATCH PREVENTING SIGNALS FROM RESETTING 68331
HOWEVER, SCOPE CAN LATCH READ SIGNALS FOR VERIFICATION.

MERLIN DRIVER - LOW REGISTER & BYTE WRITING ROUTINE
FOR ADDRESS DECODING IN PLD
CANNOT USE CS. AS VALID FOR 1 MEG SPACE ONLY
MERCIN NEEDS 4 MEG SPACE.

9-6-95

WORKED ON CEC MULTI PLATFORM PROBLEMS
CLOCK NOT BUFFERED BEFORE BEING FANDED OUT
TO MULTIPLE PORTS: CLOCK LOOKED GOOD ON SCOPE
BUT NOT ENOUGH FOR PROCESSORS.

9-19-95

ON THE BDM WHEN LOADING RAM ~~FROM~~ ON TO
A WOODWARD PRESENT RESET FOR OCCURRING
IT TRIES TO REWRITE THE VECTORS WHICH ARE IN FLASH
THIS PUTS FLASH IN 2 DUMP MODE & EXECUTABLE IS
NOT DOWNLOADED PROPERLY
ALSO ERASE THE SECOND SNEAKED WHICH ATTEMPTS
TO MODIFY THE INTR/SHCLK/ADAPTER.